

**Electronic Patent Application Fee Transmittal**

<b>Application Number:</b>	10525999			
<b>Filing Date:</b>	28-Nov-2005			
<b>Title of Invention:</b>	Method and device for verifying digital circuits			
<b>First Named Inventor/Applicant Name:</b>	Stefan Horeth			
<b>Filer:</b>	John Alfred Lingl/Janet Levoska			
<b>Attorney Docket Number:</b>	10808/193			
Filed as Large Entity				
<b>U.S. National Stage under 35 USC 371 Filing Fees</b>				
<b>Description</b>	<b>Fee Code</b>	<b>Quantity</b>	<b>Amount</b>	<b>Sub-Total in USD(\$)</b>
<b>Basic Filing:</b>				
<b>Pages:</b>				
<b>Claims:</b>				
<b>Miscellaneous-Filing:</b>				
<b>Petition:</b>				
<b>Patent-Appeals-and-Interference:</b>				
<b>Post-Allowance-and-Post-Issuance:</b>				
<b>Extension-of-Time:</b>				
Extension - 1 month with \$0 paid	1251	1	130	130

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
<b>Miscellaneous:</b>				
Request for continued examination	1801	1	810	810
<b>Total in USD (\$)</b>				<b>940</b>